

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**



Re patent application of  
Hiroyuki Kunishima, et al.

Serial No.: 10/613,069 Group Art Unit: Not Yet Assigned

Filing Date: July 7, 2003 Examiner: Unknown

For: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD  
THEREOF

Honorable Commissioner of Patent  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

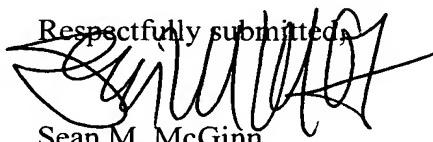
Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following document listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copy of the listed document is provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to  
Attorney's Deposit Account No. 50-0481.

Respectfully submitted,  
  
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Date: 9/02/03

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